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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/726,894	12/02/2003	Ajit Dubhashi	IR-2427 (2-3808)	4168
7590 06/17/2005				
OSTROLENK, FABER, GERB & SOFFEN, LLP		EXAMINER		
1180 Avenue of the Americas		TIBBITS, PIA FLORENCE		
New York, NY 10036-8403				
		ART UNIT	PAPER NUMBER	
		2838		

DATE MAILED: 06/17/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

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Office Action Summary	Application No.	Applicant(s)	
	10/726,894	DUBHASHI, AJIT	
	Examiner	Art Unit	
	Pia F. Tibbits	2838	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-6 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-6 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____. |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>5/6/2004</u> . | 6) <input type="checkbox"/> Other: ____. |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-4 and 6 are rejected under 35 U.S.C. 102(b) as being anticipated by **Elliott et al.** [hereinafter Elliott][5012381].

Elliott discloses in figures 2-4 a reverse battery protection circuit comprising: a first controlled semiconductor switch 34 for providing current to a load 30 and coupled in series with load terminals across which load terminals the load is adapted to be connected; and a second controlled semiconductor switch 40 disposed in a series circuit with a free wheeling diode 38, the series circuit being coupled across the load terminals [see fig.1; abstract; column 2, lines 13-23].

As to claims 1-4 and 6, see remarks and reference for claim 1.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claim 5 is rejected under 35 U.S.C. 103(a) as being unpatentable over **Elliott**, as described above, in view of **Pakkala et al.** [hereinafter Pakkala][6154081].

Elliott does not disclose a zener diode coupled between gate and source of the second controlled semiconductor.

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Pakkala discloses in the figure a zener diode 64 connected across the gate-to-source circuit of a FET 40. The zener diode 64 has a breakdown voltage of less than the rated gate-to-source voltage, so that it conducts to protect FET 40 in the event of excessive gate-to-source voltage [see column 2, lines 49-50; column 3, lines 5-8]. Therefore, it would have been obvious to a person having ordinary skill in the art at the time the invention was made to modify Elliott's apparatus and include a zener diode coupled between gate and source of the second controlled semiconductor, as disclosed by Pakkala in order to maintain a constant voltage, and to protect the FET in the event of excessive gate-to-source voltage.

Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. The prior art cited in PTO-892 and not mentioned above disclose related apparatus.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Examiner Pia Tibbits whose telephone number is (571) 272-2086. If unavailable, contact the Supervisory Patent Examiner Mike Sherry whose telephone number is (571) 272-2084. The Technology Center Fax number is (703) 872-9306.

7. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

PFT

June 15, 2005

Pia Tibbits

Primary Patent Examiner

